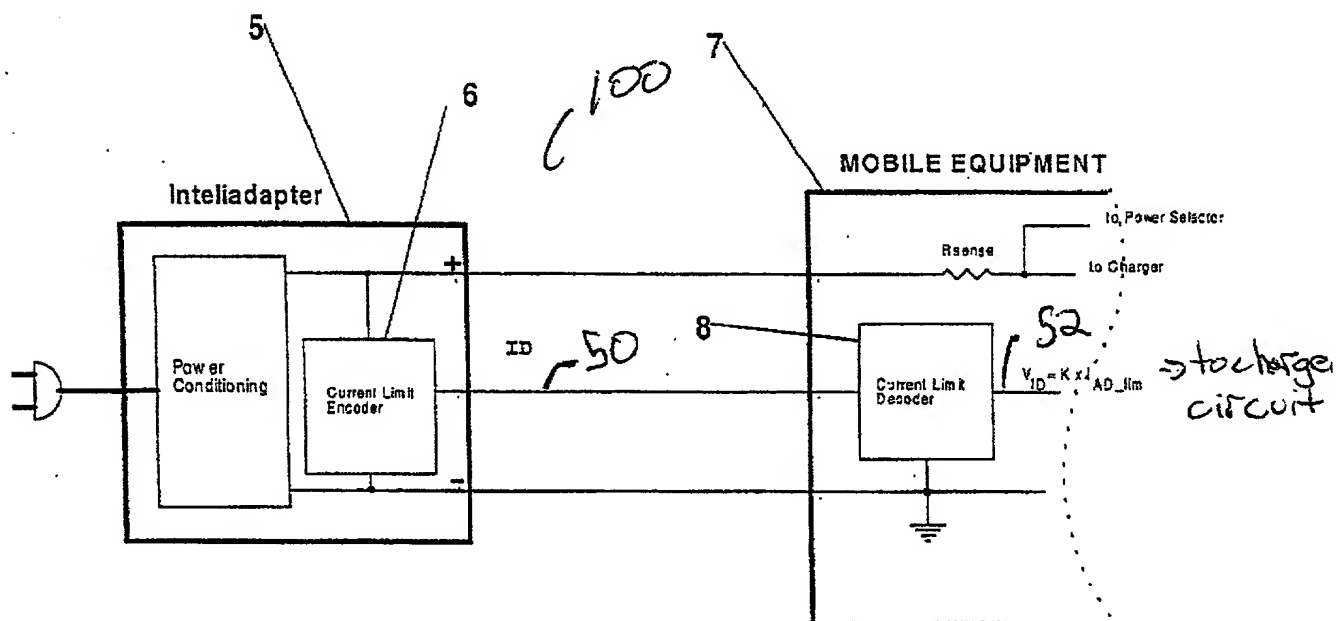


Prior Art

to power management dlc
or
charger circuit
Indicative of
maximum adapter
current
Binary

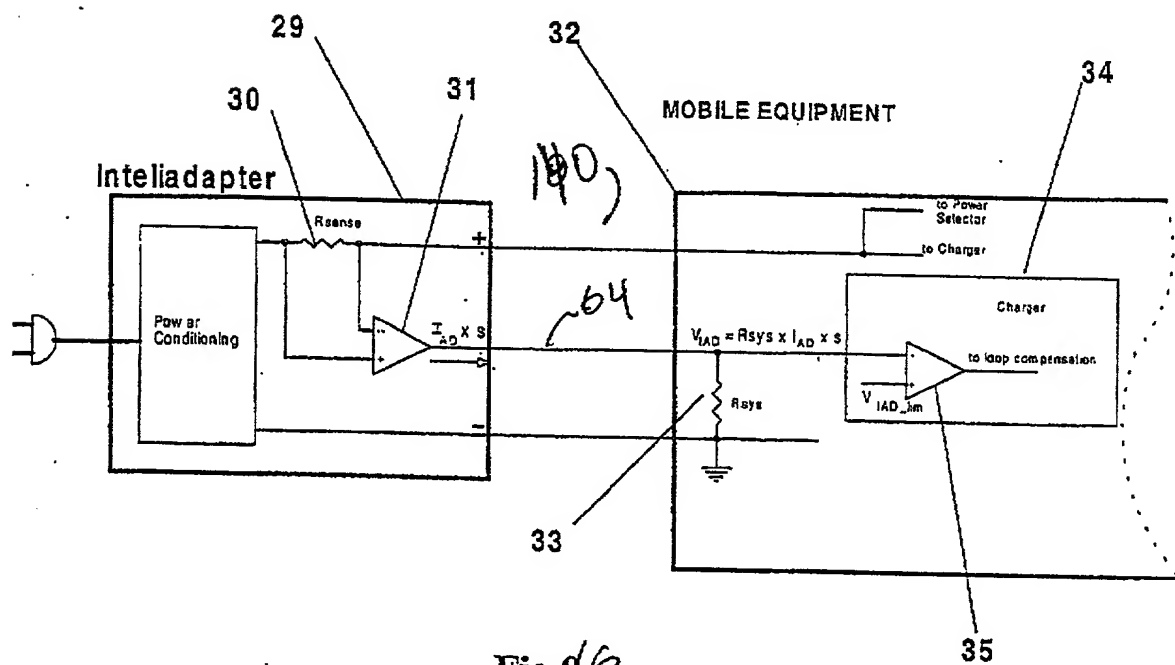
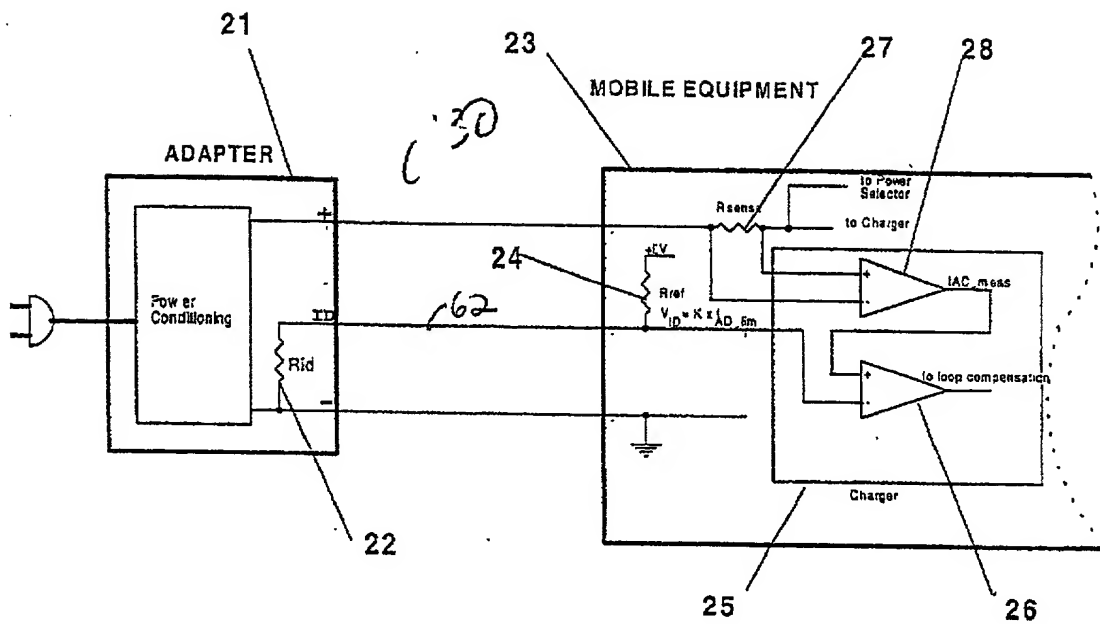


to charge
circuit

10055810 012300

Fig. 1 is a block diagram of a power adapter system. It consists of an ADAPTER (17) and MOBILE EQUIPMENT (19). The ADAPTER (17) contains a Power Conditioning block (18) and a resistor R_{id} (20). The MOBILE EQUIPMENT (19) contains a resistor R_{sense} , a resistor R_{ref} (20), and a feedback loop with gain K and current I_{AD_lim} . The output voltage V_{ID} is determined by the feedback loop. The diagram is labeled with 120.

Fig. 5/4



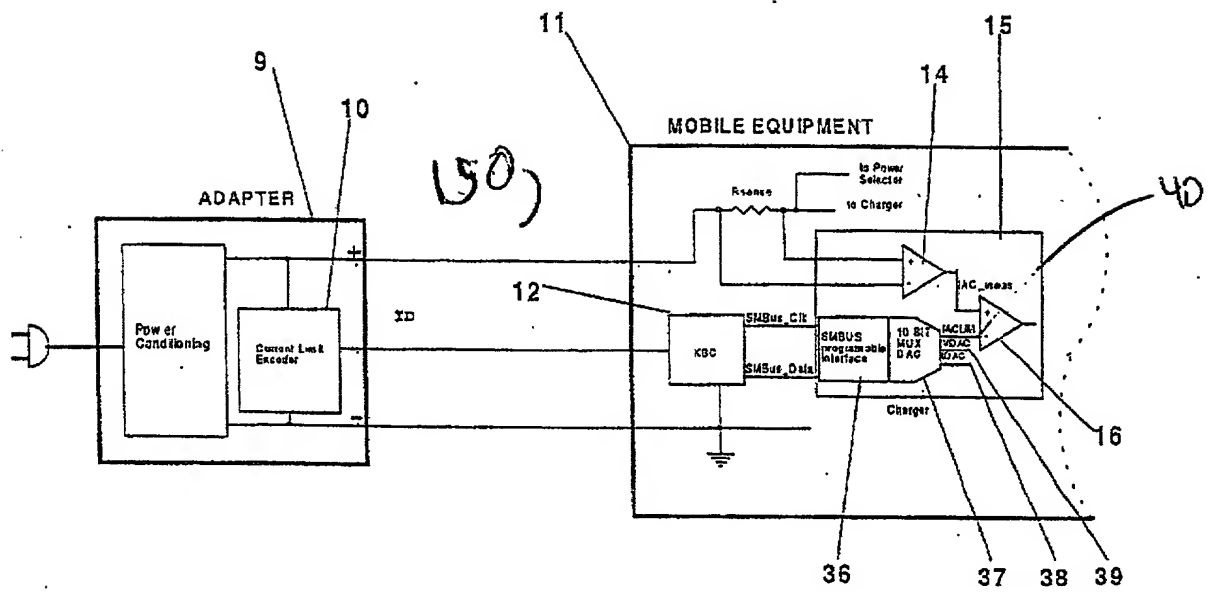


Figure 7